One Week Faculty Development Program
On
"Scilab"

Organized by,
Department of Electrical Engineering
&
Department of Computer Science and Engineering

Radhakrishna Institute of Technology and Engineering
Plot No.-l, IDCO Industrial Estate, Barunei, Khordha, Odisha, Pin-752057

In association with
Spoken Tutorial (IIT Bombay)

25th June to 29th June 2024

Coordinators

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About FDP

The FDP aimed at stimulating discussions on various research applications and new directions in the field of Scilab. Scilab is a Free/Libre and Open Source Software (FLOSS), created mainly for numerical computaions. It uses the state of the art numerical libraries, such as LINPACK, EISPACK and LAPACK, and hence, its results are highly reliable. Scilab implements the user friendly language, similar to the one used in the popular software Matlab. Scilab comes with a graphical programming interface Xcos. The FOSSEE (Free/Libre and Open Souce Software for education) project at IIT Bombay (https://fossee.in) has been promoting Scilab, and other FLOSS software.

About us

The Radhakrishna Institute of Technology and Engineering (RITE) is a JRG Educational Trust (JRGET) initiative. The trust takes its roots from the life and philosophy of noted social worker and philanthropist Shri Jagdish Raiji Gupta. The objective of the trust has been impart education to all strata of the Indian society and to equate the Indian educated youth with the heights scaled by their counterparts abroad. Apart from the primary objective, the group continues to bear the beacon of social upliftment of the backward sections of the Indian society. The trust is a home to host of eminent members of the Indian intelligentsia. The Chairman of JRGET Mr. Vineet Mohan. Gupta, is the pillars of strength, resource and support on which the trust functions.

RITE is affiliated to Biju Patnaik University of Technology, Rourkela and approved by AICTE, New Delhi, accrediated with NAAC and certified with ISO (50001, 14001, 9001).

The department of Electrical Engineering was established in 2010 with the vision of providing value based quality education and training that gives students in-depth knowledge and hands-on experience in the field of Electrical Engineering. The department offers UG Program in Electrical Engineering.

The department of Computer Science and Engineering was established in 2010 with the vision of imparting top-notch education in the field of computer science, useful for bridging the gap between academic and industry. The department offers UG Program in Computer Science & Engg.,Computer Engg. The Department also offers PG Program in Master of Computer Application.

Chief Patrons

Mr. Vineet Mohan Gupta
Chiarman, JRG Educational Trust

Ms. Rashmi Gupta
Director, JRG Educational Trust

Patrons

Dr. Rebecca Bhattacharya
Member, Management Committee, RITE

Dr. Subash Ranjan Kabat
Principal, RITE

Convenor

Dr. Chandrabhanu Malla
Dean Academics, RITE

Coordinators

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Prof. Jugajyoti Sahu
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Prof. Priyanka Shit
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Registration Starts: 11th June 2024
Registration Ends: 22nd June 2024
Registration Fees: Rs.590/- inclusive of GST*
Mode of conduction: Online (Remote Learning)
* There is no registration fee for the faculty members of RITE

Registration Link for outside participants: **Register**

Registration Link for RITE faculty members: **Register**

After registration, please join the Whatsapp Group created for this FDP using the following link:
Join Whatsapp Group